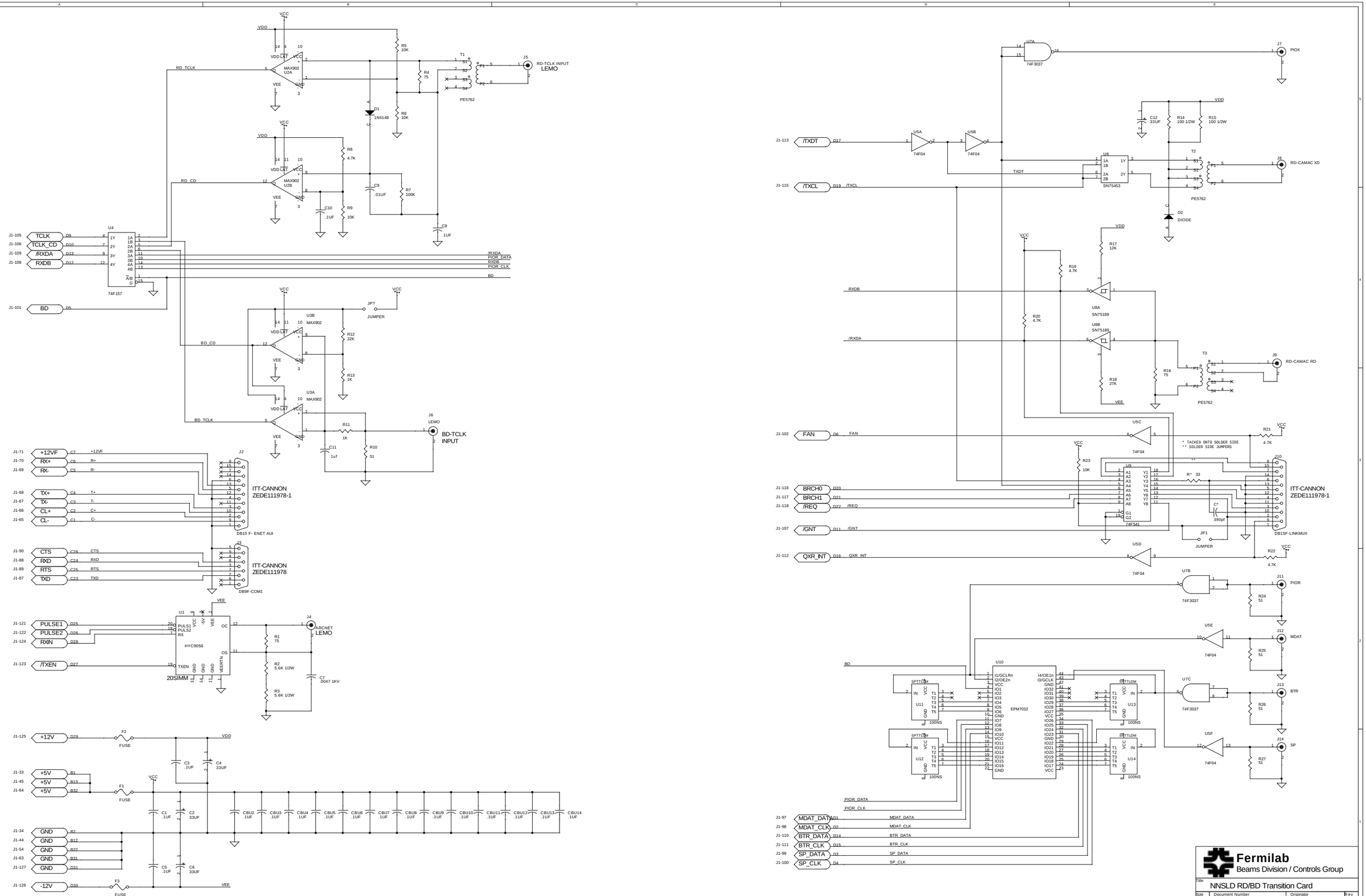

RD-TCLK INPUT
LEMO
BD-TCLK
INPUT
ITT-CANNON
ZEDE111978-1
ITT-CANNON
ZEDE111978
20SIMM
ARCNET
LEMO
RD-CAMAC XD
RD-CAMAC RD
PIOX
PIOR
MDAT
BTR
SP
DIODE
JUMPER
* TACKED ONTO SOLDER SIDE
** SOLDER SIDE JUMPERS
Fermilab
Beams Division / Controls Group
Title
NNSLD RD/BD Transition Card
Size
D
Document Number
NNSLD_IO
Originator
P. Kasley
Rev
O
Date: Friday, April 30, 1999
Sheet 1 of 2

NNSLD-IO BOARD CONFIGURATIONS

MAIN INJECTOR SWIC FRONT-END

Component side jumpers:

U2-14 to U2-10
U4-3 to J1-D20
U4-6 to J1-D21

Components Used:

U3 - MAX902
U9 - 74F541
J1 - 3 row, 96 contact reverse DIN right angle socket
J6 - LEMO
J10 - 15 pin Dsub female
F1 - 1A Picofuse
C1 - .1uf 50V MLC bypass
C2 - 33uf 25V axial aluminum electrolytic
CBU3 - .1uf 50V MLC bypass
CBU9 - .1uf 50V MLC bypass
R12 - 10K 1/4W 5% CFR
R13 - 1K 1/4W 5% CFR
R10 - 51 ohm 1/4W 5% CFR
R11 - 1K 1/4W 5% CFR

Notes:

1. J1 is a 3-row right angle SOCKET.
2. J6-3 is TCLK CARRIER DETECT. J6-4 is TCLK.
3. The VDD (+12V) bus is shorted to the VCC bus (+5V). J1 supplies only +5V to the board.
4. Component values listed above supersede those shown on the schematic.

EXPERIMENTAL AREAS CAMAC FRONT-ENDS (FIXED TARGET BEAMLINES)

Components used:

U2, U4, U5, U9
J1, J2, J5, J10
C1, C2, C3, C4, C8, C9, CBU2, CBU4, CBU5, CBU9
D1
F1, F2
T1
JP1
R4, R5, R6, R7, R8, R9, R21, R22, R23
JP1

Notes:

1. All component values are per the schematic EXCEPT R22=0 ohms.
2. J1 is a reverse DIN, right angle,160 contact socket.
3. +12V (VDD) is supplied through the NNSLD PMC card. +5V (VCC) is supplied from backplane row B. -12V (VEE) is not used.
4. J1Row D pinout (PMC) is for the MVME2600. It is not compatible with the MVME2300.
5. Fuses are 1A Picofuse or equivalent.

NNSLD_IO to LINKMUX Cable

Linkmux (26 pin ribbon)	NNSLD_IO (15 pin D)	
1	2	req
3	1	gnt
5	4	br0
7	3	br1
9	7	rxdb
11	8	rxda
13	6	txclk
15	5	txdata
all others	10,11,12,13,14	gnd

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Title			
NNSLD RD/BD Transition Card			
Size D	Document Number NNSLD_IO	Originator P. Kasley	Rev O
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