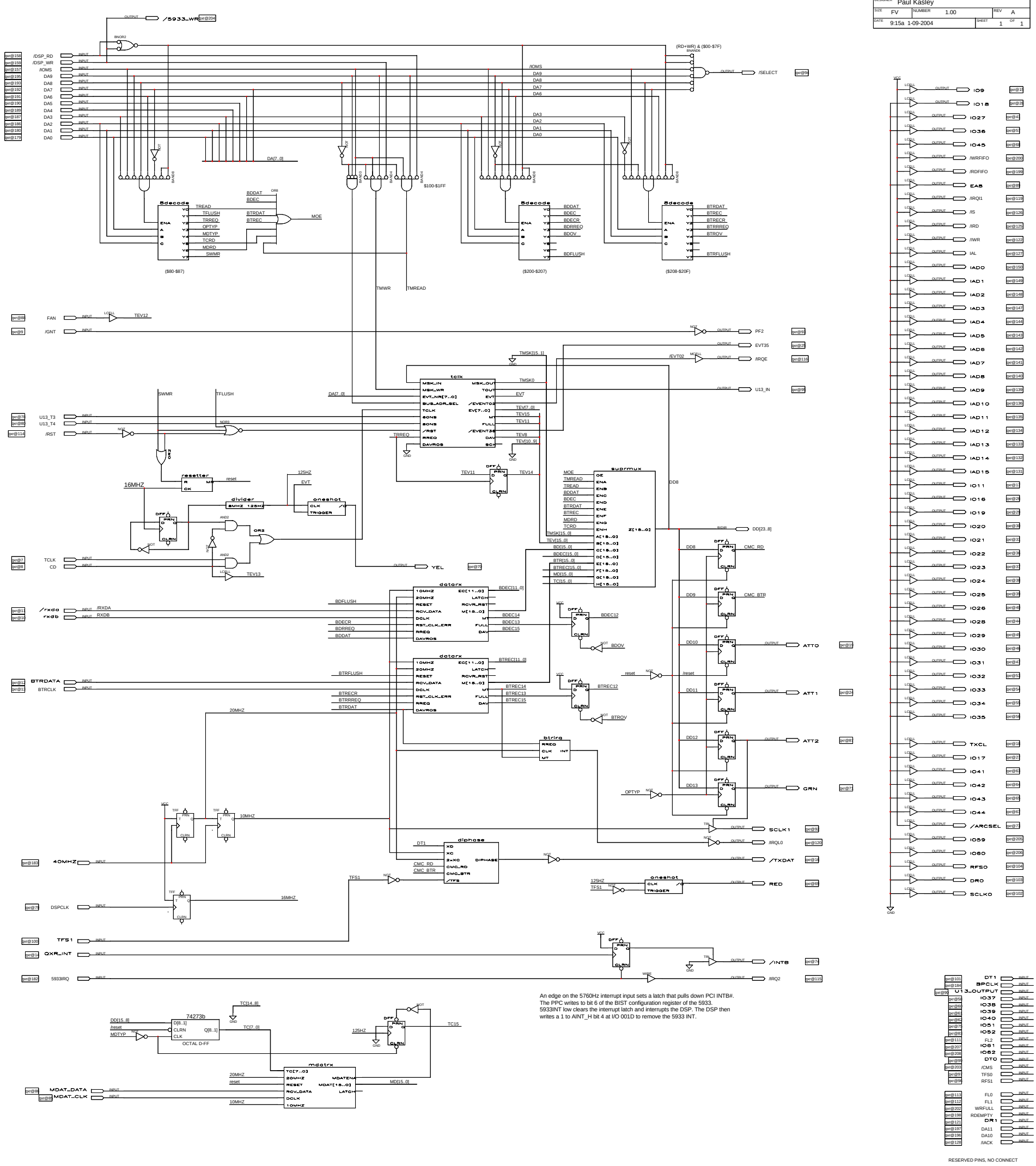
TITLE	QXR CAMAC FOR REV C NNSLD
COMPANY	Fermilab
DESIGNER	Paul Kasley
SIZE FV	NUMBER 1.00	REV A
DATE 9:15a 1-09-2004	SHEET 1 OF 1



An edge on the 5760Hz interrupt input sets a latch that pulls down PCI INTB#.
The PPC writes to bit 6 of the BIST configuration register of the 5933.
5933INT low clears the interrupt latch and interrupts the DSP. The DSP then writes a 1 to AINT_H bit 4 at I/O 001D to remove the 5933 INT.