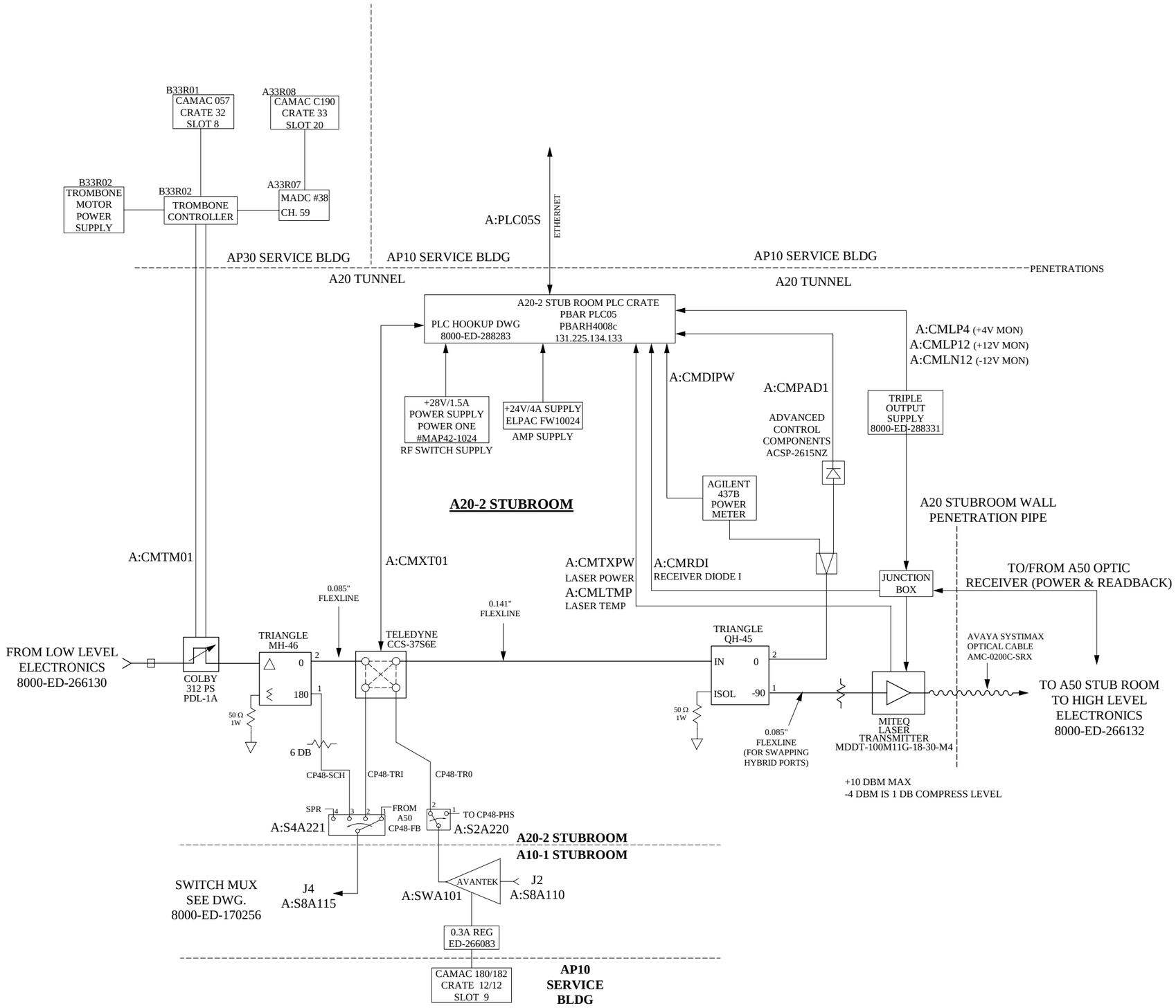
REV.	DESCRIPTION	DRAWN / APPD.	DATE / DATE
B	REDRAWN AND UPGRADED TO 4-8 GHZ	G.A. JOHNSON / W.J. MUELLER	8/1/90 / 10/9/90
C	ADDED PIN SYNC CHASSIS	G.A. JOHNSON / W.J. MUELLER	3/21/91 / 3/25/91
D	PIN SWITCH TIMING CHANNELS	G.A. JOHNSON / W.J. MUELLER	9/19/91 / 9/24/91
E	ADDED PIN ATT., LIMIT, & DIODE NAMES	G.A. JOHNSON / W.J. MUELLER	11/5/92 / 11/9/92
F	ADDED PHASE EQUALIZER GRAPH	G.A. JOHNSON / W.J. MUELLER	6/23/93 / 7/8/93
G	REDRAWN ON MINI-CAD	W.J. MUELLER / W.J. MUELLER	3/22/95 /
H	NAME/CONTROL CHANGES	W.J. MUELLER / W.J. MUELLER	8/14/98 /
J	DELAY SWITCHES AND EQUALIZER REMOVED	W.J. MUELLER / W.J. MUELLER	10/10/01 /
K	DIODE PAD CHANGE	W.J. MUELLER / W.J. MUELLER	5/4/05 /
L	PIN ATT & SW MOVED TO HI LEVEL, RE-MADE PLATE, OPTICS INSTALLED	W.J. MUELLER / W.J. MUELLER	4/18/06 /



NOTES:

1) AMPLIFIER GAINS ARE NOMINAL.
2) AMPLIFIER POWER SUPPLY REGULATOR BUS HAS: ON/OFF CONTROL, HIGH/LOW VOLTAGE STATUS, AND HIGH/LOW CURRENT STATUS.
3) TROMBONE LINE BUS HAS MOTOR CONTROL LINES, LIMIT SWITCH LINES AND POSITION READBACK.
4) BOLD LINES INDICATE MAIN RF PATH.

8/22/06

ITEM NO.	PART NO.	DESCRIPTION OR SIZE	QTY. REQ.
PARTS LIST			

UNLESS OTHERWISE SPECIFIED			ORIGINATOR	R.J. PASQUINELLI	
FRACTIONS	DECIMALS	ANGLES	DRAWN	G.A. JOHNSON	8/1/90
+/-	+/-	+/-	CHECKED	WESLEY MUELLER	10/9/90
			APPROVED		

1. BREAK ALL SHARP EDGES 1/64 MAX.
2. DO NOT SCALE DRAWING
3. DIMENSIONING IN ACCORD WITH ANSI Y14.5 STD'S

MAX. ALL MACHINED SURFACES

USED ON

MATERIAL-

FERMI NATIONAL ACCELERATOR LABORATORY
UNITED STATES DEPARTMENT OF ENERGY

ANTI-PROTON SOURCE
CORE MOMENTUM (4-8 GHZ)
MEDIUM LEVEL ELECTRONICS

SCALE	FILMED	DRAWING NUMBER	REV.
		8000-ED-266131	L